Specifications

Products Name	SMT High Voltage Metal Thick Film Chip Resistor
Product Series	LRT***V
Classification	Generic Specification





SMT High Voltage Metal Thick Film Chip Resistor Specification

<u>Scope</u>

This specification applies to SMT High Voltage Metal Thick Film Low Ohmic Chip Resistor

Part Number

LRT	6432	V	W	625		F
Part	Size	Characteristic	Termination	Resistance		Tolerance
Series	5025(2010) 6432(2512)	V: High Voltage Handling	W: With Side Termination	E24 series	3digits	F: ±1% J: ±5%

Electrical Specification

Size	Rated power (W) Jumper: Rated Current(A)	Range of resistance(Ω)	Tolerance (Nominal Resistance Series)	TCR	Max. Operating Voltage(V)	Max. Overload Voltage(V)
5025	0.5	1 1014	F: ±1%, J: ±5%	±100ppm/deg.C	2000	3000
6432	1	1 1-10101	(E-24)		3000	4000

<Ratings>

Parameter	Specification			
Rated Ambient temperature	+70 deg. C Refer to Derating curve, Figure-1			
Rated Operating Temperature Range	-55~+155 deg. C			
Rated Voltage	$\sqrt{\text{Power x Resistance}}$ (V)			

Figure-1



Structure/Dimensions/Marking

<Structure>

This part has a structure that metal glazed resistor is formed on ceramic substrate with the termination layers interconnected, and the passivation coated (See figure-2) under construction and composition as shown in the chart in the below (See Figure-3).

Figure-2



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	$IIr \triangle \prec$	
1 10	uic-0	

Symbol	Construction	Composition
(1)	Substrato	A12O3(06%)
(2)	Inner Electrode	Ag-Pa Thick Film
3	Resistor	Ru-O2 Thick Film
4	Protection coating	Glass passivation
5	2 nd Side Electrode	Ni plating
6	3 rd Side Electrode	Sn plating

<Dimension>

Figure-4



Figure-5

	Dimension (mm)						
Size	L	W	а	b	t		
5025	$5.0\!\pm\!0.2$	2.50 ± 0.15	0.6 ± 0.2	$0.5\!\pm\!0.3$	$0.6\!\pm\!0.1$		
6432	6.3 ± 0.2	3.2 ± 0.2	0.7 ± 0.2	0.7 ± 0.2	0.6 ± 0.1		

<Marking>

E24 series : 3 digits Example) 103 : $10 \times 10^3 = 10,000(Ω) = 10k(Ω)$

Reliability Characteristics

Figure-6

Item	Condition	Specification		
Object Times Objects and	Loading power=2.5 times x the rated v	oltage	10.00/	
Short Time Overload	Loading time = 5sec.		±2.0%	
	Thermostatic Chamber = 155±3 deg. C		14.00/	
Hign Temp. Exposure	Retention time = $1000 \text{ h} \pm \frac{48}{0}$ Hrs.		±1.0%	
. <u>.</u> .	Thermostatic Chamber=-55±3 deg. C		.4.0%	
Low Temp. Exposure	Retention time = 1000 h $\pm \frac{48}{0}$ hrs.		±1.0%	
	Thermostat Chamber=40°C±2 deg. C S	90~95%RH		
Temp./Humidity Biased	Loading voltage = Rated voltage		10.00/	
Test	A cycle = 90min. on \sim 30mi. off		±3.0%	
	Retention time cycled = 1000 h $\pm \frac{48}{0}$ hrs			
	A series of dwell time at each stage in			
	the below cycles;			
Thermool Quelo	Stage 1=-55°C±3 deg. C for 30min.	100 avalas	14.00/	
Thermal Cycle	Stage $2 = RT$ within 3 min.	TOU cycles	±1.U%	
	Stage 3=+155±3 deg. C for 30min.			
	Stage4 = RT within 3 min.			
	Thermostat Chamber=70±3 deg. C			
Load Life	Loading voltage = Rated voltage	+3.0%		
	Retention time = 90min. on \sim 30min.off	13.0 %		
	$1000 \mathrm{h} \pm \frac{48}{0}$ hrs.			
	Solder Temp. = 245±5 deg. C			
O - Lile werk litter	Soaking time=3±0.5sec.	New soldered coverage takes up		
Solderability	Pre-conditioning=immersing in flux for	95% of terminations		
	Flux=IPA solution with 25% weight ratio			
	Solder Temp.=260±5 deg. C (Molten s			
Soldering Heat Resistance	Soaking time = 10±1sec.	No solder leach observed		
	A distance between two supporting poir	nts: 90mm		
Poord Flox	Flex depth : 1mm	+1 00/		
Board Flex	Board:Glass Epoxy t =1.6mm	±1.070		
	Retention time=10±1sec.			
	Applied voltage : DC100V±15V under t	he setup shown		
Insulation	in the below for 1min and measure resis	1,000M Ω or over		
	(from termination to substrate)			

Dielectric withstanding below for 60±5s.	r the setup shown in the and dielectric breakdown. leak current: ≦2mA
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Figure-7 Measurement Setup for insulation resistance/dielectric withstanding voltage



Packaging



Size	A	В	VV	F	E	P1	P2	P ₀	D_0	l1	T2
5025	2.90±0.10	5.40±0.10	10.00.0.00		1 75 0 10	1 00 0 10	0.00.0.05	4.00 0.10	1 55 0 05	1.10	0.05
6432	3.40±0.10	6.60±0.10	12.00±0.20	5.50±0.05	1.75±0.10	4.00±0.10	2.00±0.05	4.00±0.10	1.55±0.05	1.10max	0.25max
D 1	D'. 1 . 1		10 1	(DO						/TT •.)

Remark: Pitch tolerance over any 10 pitches of P0 is ± 0.2 mm.

(Unit: mm)

Figure-9 Finish Specification of Leading end



4 / 6

Figure -10 Cover Tape Peel-off Strength

F = Peel-off Strength : 0.1-1.0N (10-100gf)



Figure-11 Reel Configuration(Plastic Reel)



Material: Plastic

Denote	6432
SPQ	4000
φ A	180 ± 3.0
ϕ B	60.0 ± 1.0
φ C	13.0 ± 0.2
<i>φ</i> D	21.0 ± 0.8
Е	2.0 ± 0.5
W1	13.0 ± 0.3
W2	15.4 ± 0.1

(Unit: mm)

Figure-12 Labelling

LRT6432VW625F) ←	Part number
QUANTITY 4,000pcs	←	Quantity
INSPTECTED F	←	Manufacturing Month
Y.E.D. CO., LTD.	←	Manufacture
70223408	←	Lot number
MADE IN JAPAN) ←	Country of origin

Soldering Temperature Profile

Figure-13

Reflow profile (max. 3 cycles)



	Lead-freesolder Sn-3.0Ag-0.5Cu
Preheat.	150~200°C, 60~120sec.
Reflow	Min. 217°C, 60~150sec
Peak.	255~260°C, 30sec Max.

5 / 6



Storage

The products should be kept packed and stored at temperature of 15~35°C and a humidity 25~85%RH. The products should not be left in the place affected by direct sunlight and harmful gas (chlorine, sulfur, etc.). Warranty period: 1 year after shipping date.

***** E&OE *****